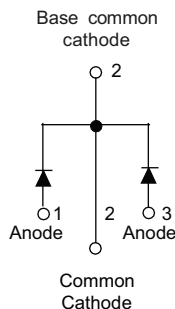
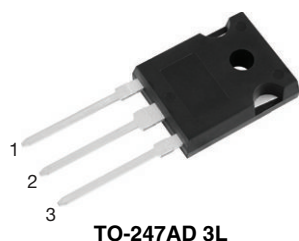


1200 V Gen 4 Power Silicon Carbide Schottky Diode, 2 x 15 A



FEATURES

- Positive V_F temperature coefficient for easy paralleling
- Virtually no recovery tail and no switching losses
- Temperature invariant switching behavior
- 175 °C maximum operating junction temperature
- Meets JESD 201 class 1A whisker test
- Solder bath temperature 275 °C maximum, 10 s per JESD 22-B106
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

LINKS TO ADDITIONAL RESOURCES



3D Models

PRIMARY CHARACTERISTICS

I_F	2 x 15 A
V_R	1200 V
V_F at I_F at 25 °C, typ.	1.36 V
T_J max.	175 °C
I_R at V_R at 175 °C	200 μ A
Q_C ($V_R = 800$ V)	77 nC
Package	TO-247AD 3L
Circuit configuration	Common cathode

DESCRIPTION / APPLICATIONS

Wide band gap SiC based 1200 V Schottky diode, designed for high performance and ruggedness.

Optimized for extreme high speed hard switching across a wide temperature range. This SiC diode is ideal for applications with high dI/dt such as high efficiency PFC and ultra-high frequency output rectifiers in AC/DC and DC/DC converters.

MECHANICAL DATA

Case: TO-247AD 3L

Molding compound meets UL 94 V-0 flammability rating
Base P/N-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

Mounting torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25$ °C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage per leg	V_{RRM}		1200	V
Continuous forward current per leg	$I_F^{(1)}$	$T_C = 158$ °C (DC)	15	A
	$I_F^{(2)}$	$T_C = 147$ °C (DC)	15	A
DC blocking voltage per leg	V_{DC}		1200	V
Repetitive peak forward current per leg	I_{FRM}	$T_C = 25$ °C, $f = 50$ Hz, square wave, DC = 25 %	66	A
Non-repetitive peak forward surge current per leg	I_{FSM}	$T_C = 25$ °C, $t_p = 10$ ms, half sine wave	75	A
		$T_C = 110$ °C, $t_p = 10$ ms, half sine wave	53	
Power dissipation per leg	$P_{tot}^{(1)}$	$T_C = 25$ °C	250	W
		$T_C = 110$ °C	108	
	$P_{tot}^{(2)}$	$T_C = 25$ °C	150	W
		$T_C = 110$ °C	65	
I^2t value	$\int i^2 dt$	$T_C = 25$ °C	28.1	A ² s
		$T_C = 110$ °C	13.8	
Operating junction and storage temperatures	$T_J^{(3)}, T_{Stg}$		-55 to +175	°C

Notes

(1) Based on typical R_{th}

(2) Based on maximum R_{th}

(3) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

**ELECTRICAL SPECIFICATIONS** ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Forward voltage per leg	V_F	$I_F = 15\text{ A}$	-	1.36	1.5	V
		$I_F = 15\text{ A}, T_J = 150\text{ }^{\circ}\text{C}$	-	1.75	2.15	
		$I_F = 15\text{ A}, T_J = 175\text{ }^{\circ}\text{C}$	-	1.88	-	
Reverse leakage current per leg	I_R	$V_R = V_R\text{ rated}$	-	6.6	100	μA
		$V_R = V_R\text{ rated}, T_J = 150\text{ }^{\circ}\text{C}$	-	93	650	
		$V_R = V_R\text{ rated}, T_J = 175\text{ }^{\circ}\text{C}$	-	200	-	
Total capacitance per leg	C	$V_R = 1\text{ V}, f = 1\text{ MHz}$	-	889	-	pF
		$V_R = 800\text{ V}, f = 1\text{ MHz}$	-	53	-	
Total capacitive charge per leg	Q_C	$V_R = 800\text{ V}, f = 1\text{ MHz}$	-	77	-	nC

THERMAL - MECHANICAL SPECIFICATIONS ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case	per leg R_{thJC}		-	0.6	1	$^{\circ}\text{C}/\text{W}$
	per device R_{thJC}		-	0.3	0.5	$^{\circ}\text{C}/\text{W}$
Marking device				4C30CP12L		

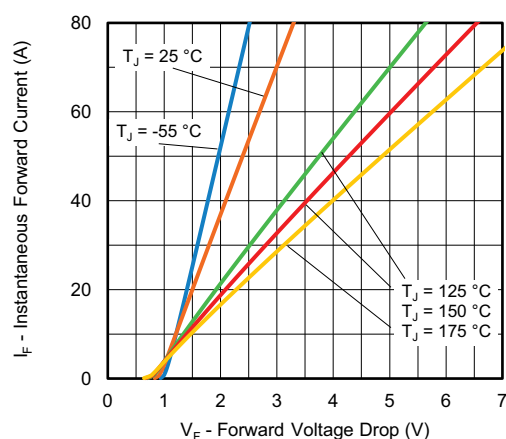


Fig. 1 - Typical Forward Voltage Drop Characteristics Per Leg

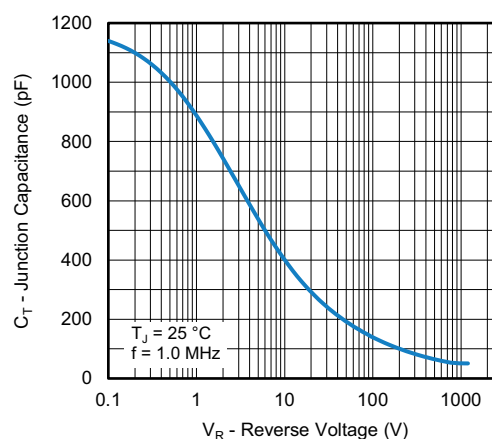


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage Per Leg

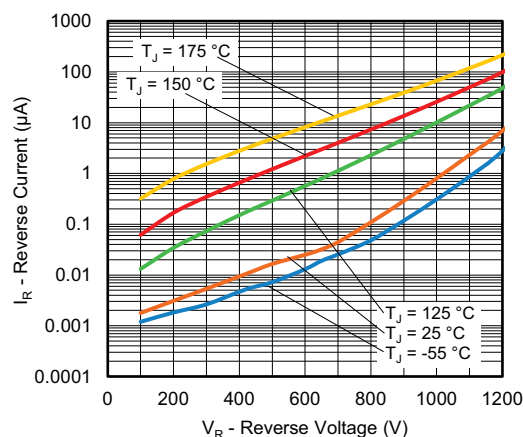


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage Per Leg

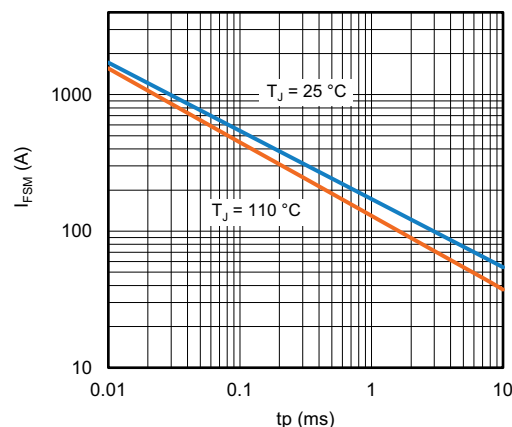


Fig. 4 - Non-Repetitive Peak Forward Surge Current vs. Pulse Duration (Square Wave) Per Leg

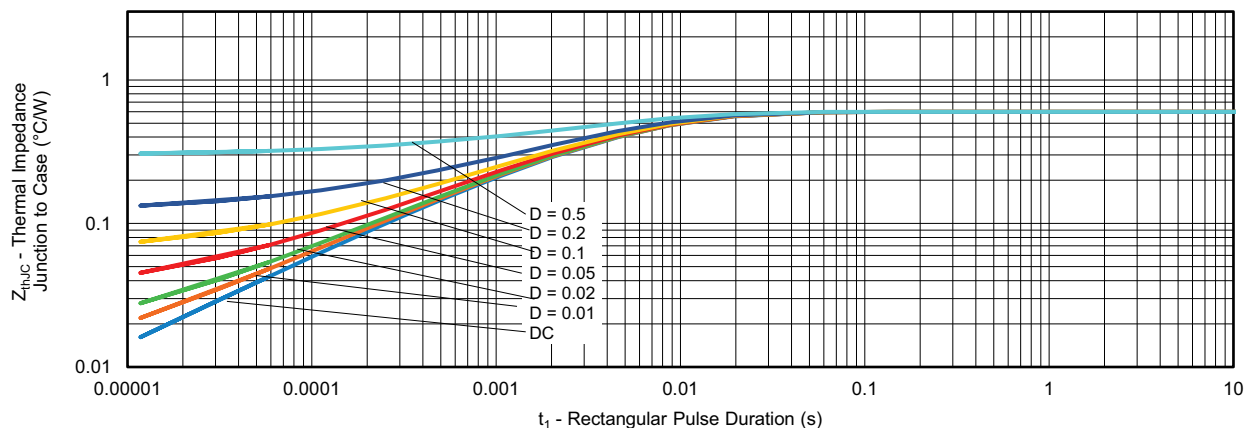
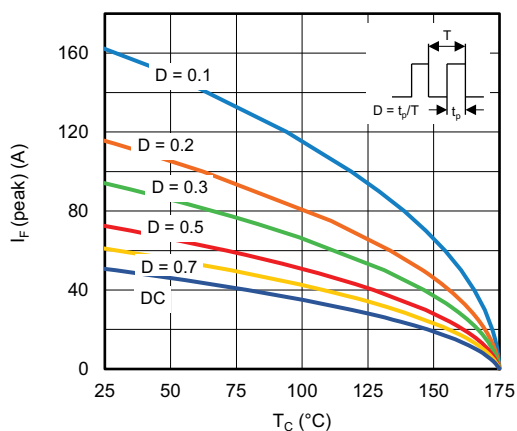

Fig. 5 - Typical Thermal Impedance Z_{thJC} - Characteristics Per Leg


Fig. 6 - Peak Forward Current vs. Maximum Allowable Case Temperature Per Leg

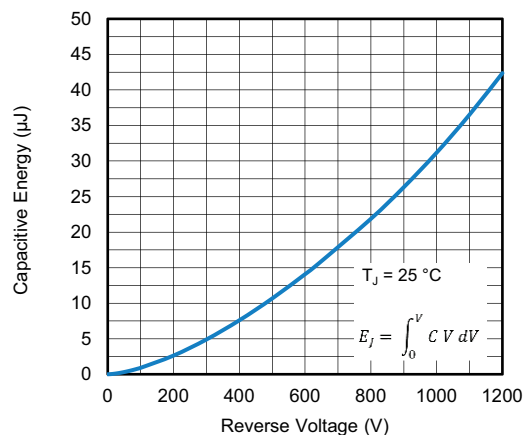


Fig. 8 - Typical Capacitive Energy vs. Reverse Voltage Per Leg

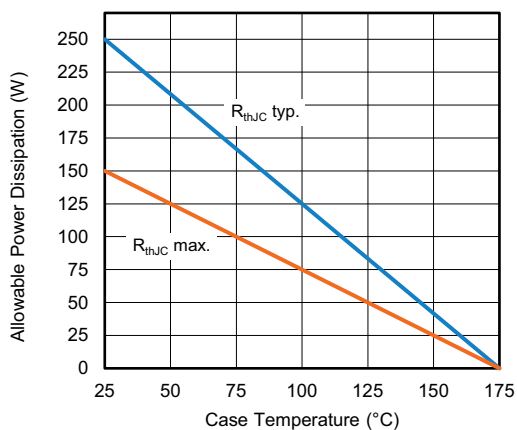


Fig. 7 - Forward Power Loss Characteristics Per Leg

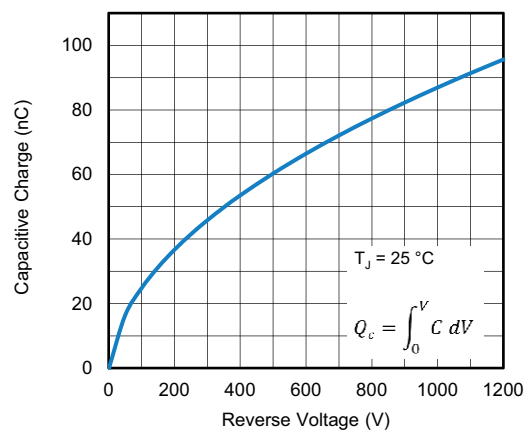


Fig. 9 - Typical Capacitive Charge vs. Reverse Voltage Per Leg

**ORDERING INFORMATION TABLE**

Device code	VS-	4C	30	C	P	12	L	-M3
	1	2	3	4	5	6	7	8
1	- Vishay Semiconductors product							
2	- 4C = SiC diode, Generation 4							
3	- Current rating (30 = 30 A)							
4	- C = common cathode							
5	- P = package TO-247							
6	- Voltage rating: 12 = 1200 V							
7	- L = long lead							
8	- Environmental digit: M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free							

ORDERING INFORMATION

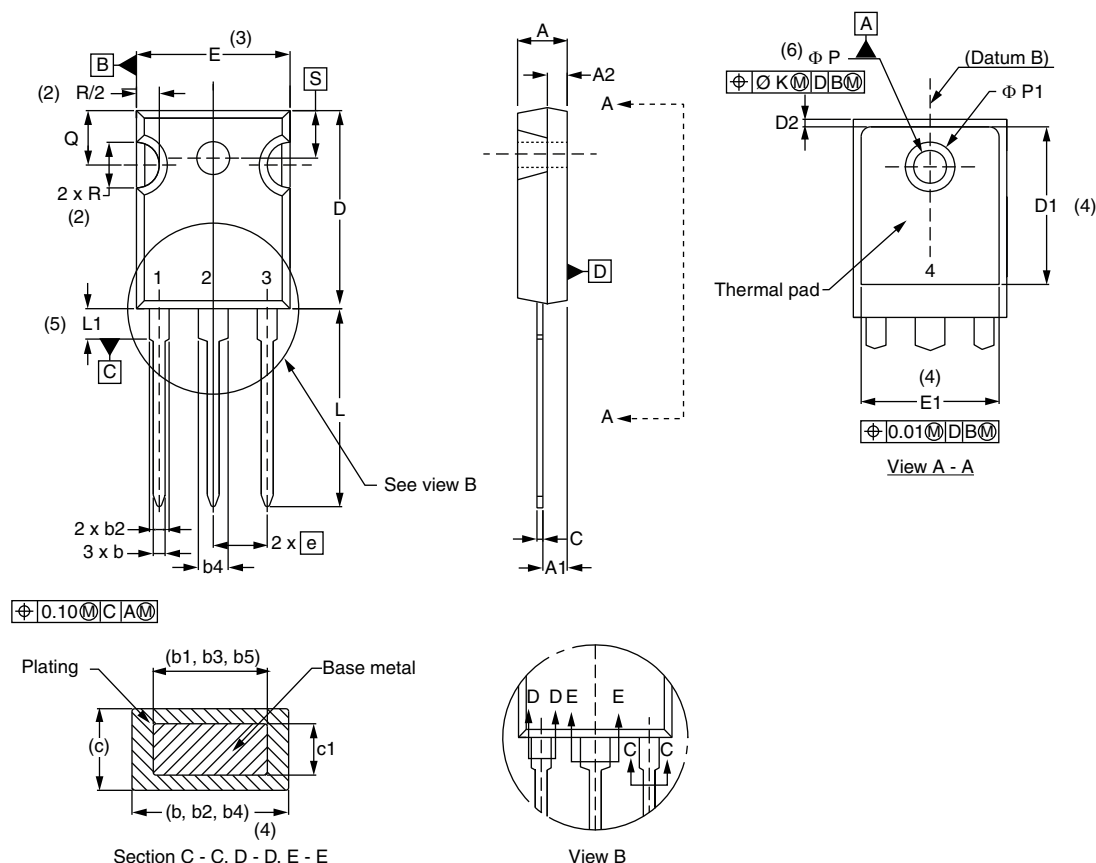
PREFERRED P/N	UNIT WEIGHT	BASE QUANTITY	PACKAGING DESCRIPTION
VS-4C30CP12L-M3	5.5 g	25 / tube	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS

Dimensions	www.vishay.com/doc?95626
Part marking information	www.vishay.com/doc?95007

TO-247AD 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
c	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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