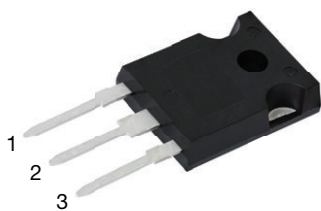
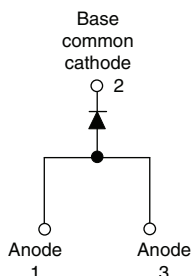


High Performance Schottky Rectifier, 65 A


TO-247AC 3L


FEATURES

- 125 °C T_J operation ($V_R < 5$ V)
- Single diode configuration
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc299912



RoHS
COMPLIANT
HALOGEN
FREE

DESCRIPTION

The VS-65PQ015... Schottky rectifier module has been optimized for ultralow forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	65 A
V_R	15 V
V_F at I_F	0.46 V
I_{RM} max.	870 mA at 100 °C
T_J max.	125 °C
E_{AS}	9 mJ
Package	TO-247AC 3L
Circuit configuration	Single

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	65	A
V_{RRM}		15	V
I_{FSM}	$t_p = 5 \mu s$ sine	1500	A
V_F	65 A _{pk} , $T_J = 125$ °C	0.46	V
T_J	Range	-55 to +125	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VS-65PQ015-N3	UNITS
Maximum DC reverse voltage	V_R	$T_J = 100$ °C	15	V
		$T_J = 125$ °C	5	

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	$I_{F(AV)}$	50 % duty cycle at $T_C = 83$ °C, rectangular waveform	65	A
Maximum peak one cycle non-repetitive surge current	I_{FSM}	5 μs sine or 3 μs rect. pulse	1500	
		10 ms sine or 6 ms rect. pulse	400	
Non-repetitive avalanche energy	E_{AS}	$T_J = 25$ °C, $I_{AS} = 2$ A, $L = 4.5$ mH	9	mJ
Repetitive avalanche current	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical	2	A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Forward voltage drop	$V_{FM}^{(1)}$	65 A	$T_J = 25\text{ }^{\circ}\text{C}$	0.50	V
		130 A		0.71	
		65 A	$T_J = 125\text{ }^{\circ}\text{C}$	0.46	
		130 A		0.76	
Reverse leakage current	$I_{RM}^{(1)}$	$T_J = 125\text{ }^{\circ}\text{C}$	$V_R = 5\text{ V}$	1.2	A
		$T_J = 25\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	18	mA
		$T_J = 100\text{ }^{\circ}\text{C}$		870	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.137	mV
Forward slope resistance	r_t			4.9	mΩ
Maximum junction capacitance	C_T	$V_R = 5\text{ V}_{DC}$ (test signal range 100 kHz to 1 MHz) $25\text{ }^{\circ}\text{C}$		4300	pF
Typical series inductance	L_S	Measured lead to lead 5 mm from package body		8	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/μs

Note(1) Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J		-55 to 125	°C
Maximum storage temperature range	T _{Stg}		-55 to 150	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.8	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.3	
Approximate weight			6	g
			0.21	oz.
Mounting torque	minimum	Non-lubricated threads	6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style TO-247AC 3L	65PQ015	

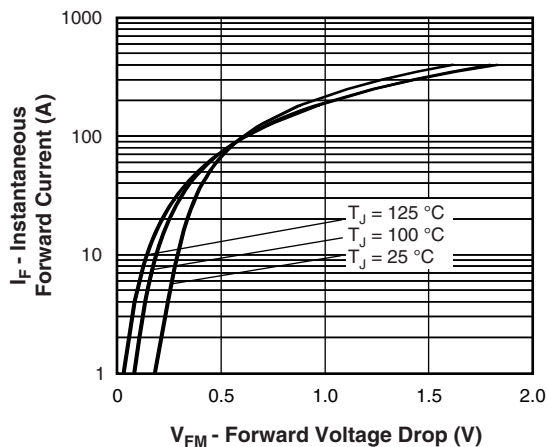


Fig. 1 - Maximum Forward Voltage Drop Characteristics

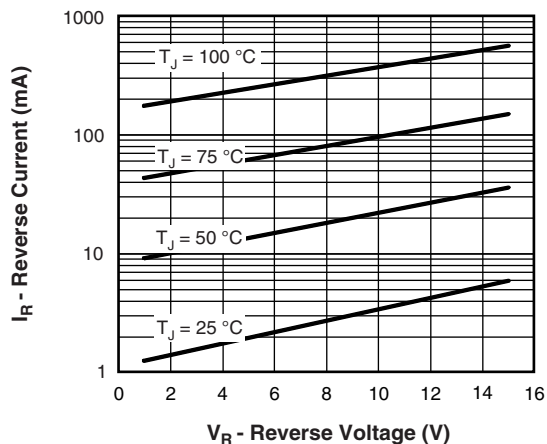


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

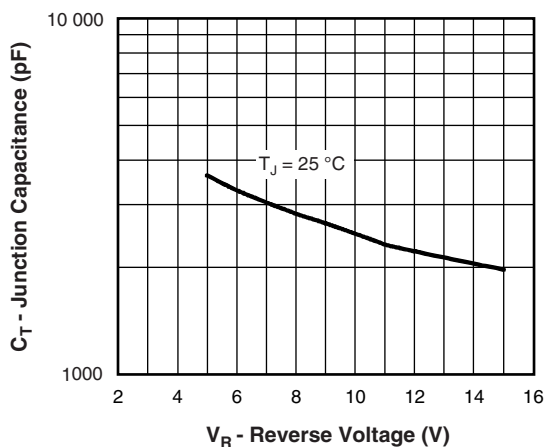
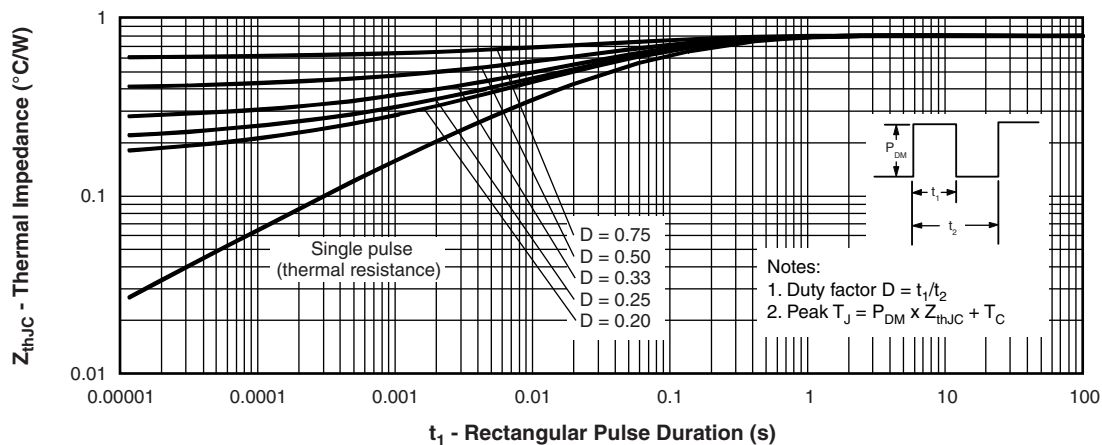


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

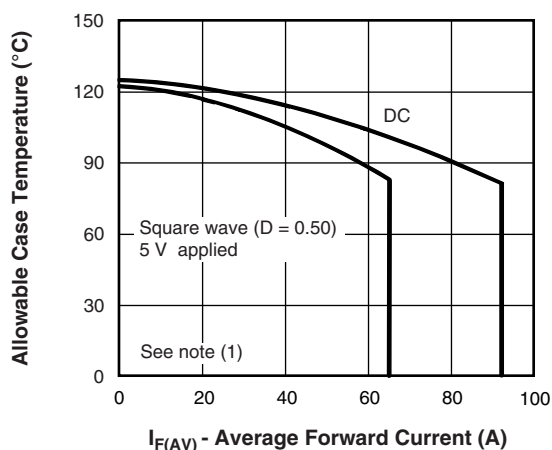


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

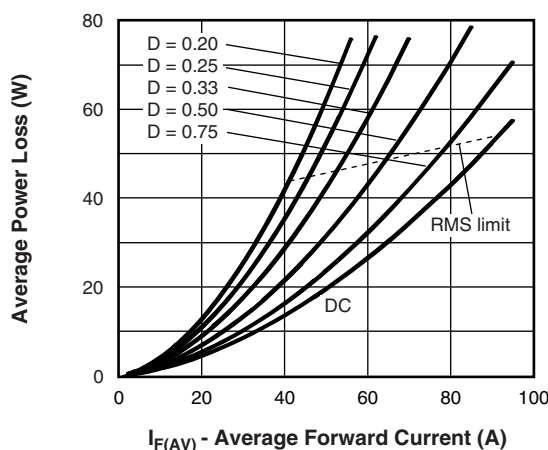


Fig. 6 - Forward Power Loss Characteristics

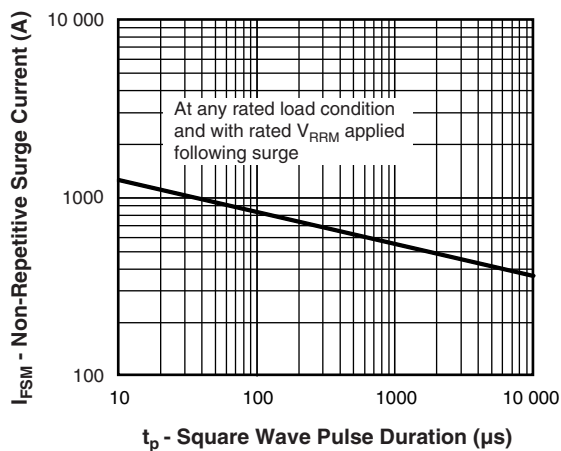


Fig. 7 - Maximum Non-Repetitive Surge Current

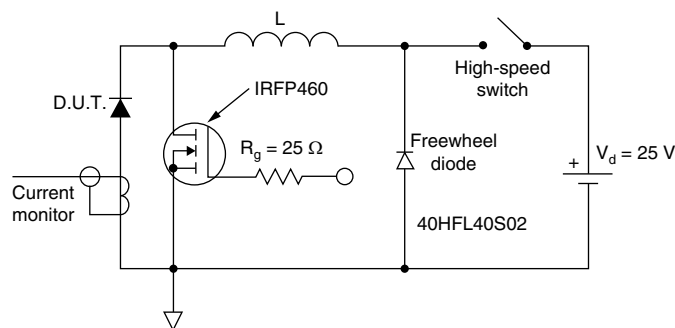


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 5$ V



ORDERING INFORMATION TABLE

Device code	VS-	65	P	Q	015	-N3
	1	2	3	4	5	6

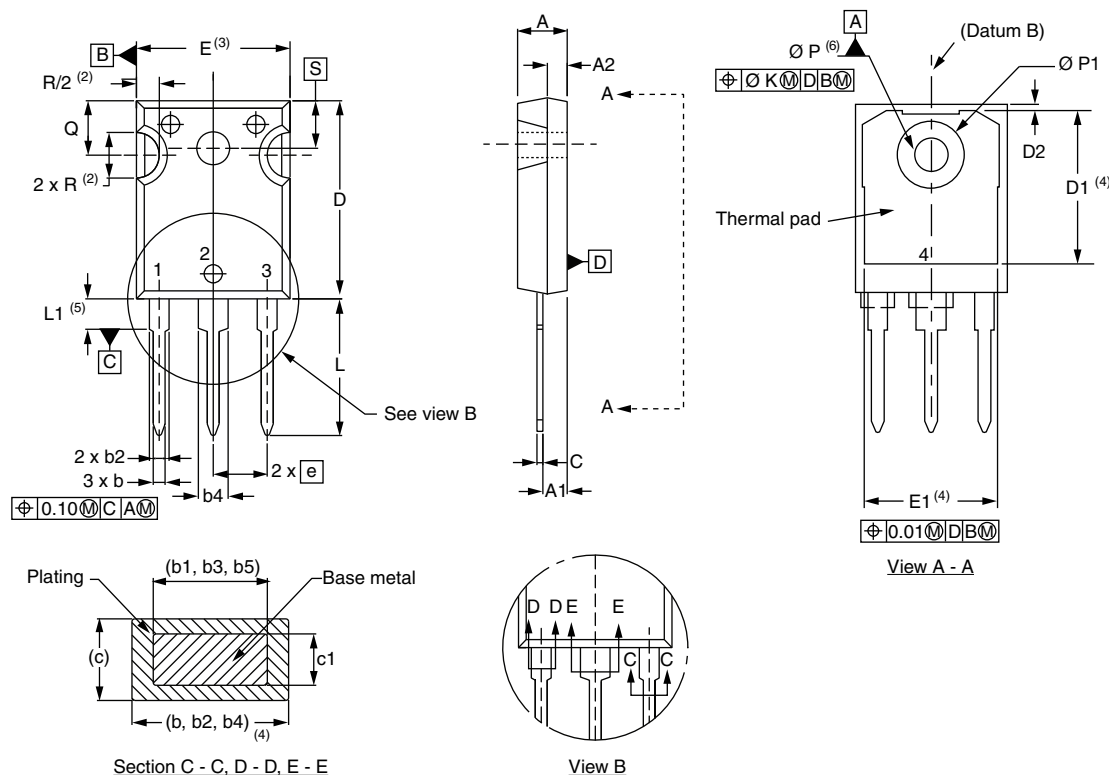
- 1 - Vishay Semiconductors product
- 2 - Current rating (65 = 65 A)
- 3 - Package:
P = TO-247
- 4 - Schottky "Q" series
- 5 - Voltage code (015 = 15 V)
- 6 - Environmental digit
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-65PQ015-N3	25	500	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96138
Part marking information	www.vishay.com/doc?95007
SPICE model	www.vishay.com/doc?97120

TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
c	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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