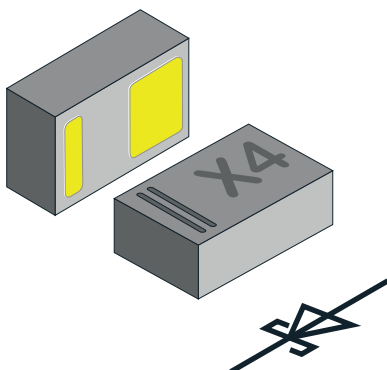


Small Signal Schottky Diode FlipKY® Gen 3



FEATURES

- Schottky diode for high-speed switching
- Very low dimensions:
0.8 mm x 0.5 mm x 0.29 mm
- 0.3 A forward current
- Low forward voltage drop (typ. 400 mV at 300 mA)
- Low reverse current (< 4 µA at 10 V)
- AEC-Q101 qualified
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912



LINKS TO ADDITIONAL RESOURCES



PARTS TABLE

PART	GRADE	ORDERING CODE	CIRCUIT CONFIGURATION	PACKAGE NAME	TYPE MARKING	WEIGHT	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY
VSKY034085	AEC-Q101	VSKY034085HG4-08	Single	CLP0805-2L	X4	0.271 mg	10 000	10 000

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V _R	40	V
Forward continuous current		I _F	300	mA
Surge forward current	Single pulse; 8.3 ms half sine-wave	I _{FSM}	9	A

THERMAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Thermal resistance junction to soldering point	Acc. JEDEC® JESD51-14	R _{thJS}	12	K/W
Maximum operating junction temperature		T _j max.	150	°C
Storage temperature range		T _{stg}	-65 to +150	°C

ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	TYP.	MAX.	UNIT
Leakage current	V _R = 10V	I _R	-	4	µA
Leakage current	V _R = 40V	I _R	-	18	µA
Forward voltage	I _F = 10 mA	V _F	272	330	mV
Forward voltage	I _F = 100 mA	V _F	346	407	mV
Forward voltage	I _F = 300 mA	V _F	400	485	mV
Diode capacitance	V _R = 0 V, f = 1 MHz	C _D	100	-	pF

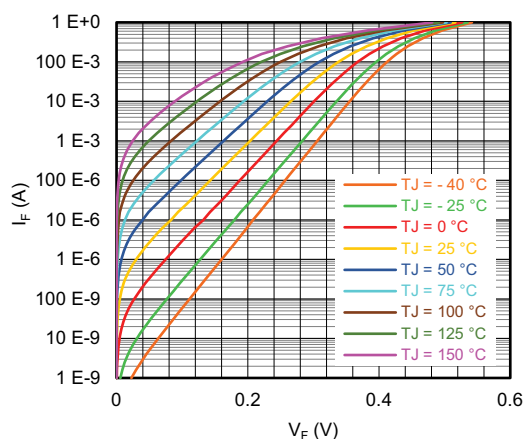
RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)


Fig. 1 - Typical Forward Current vs. Forward Voltage at Various Temperatures

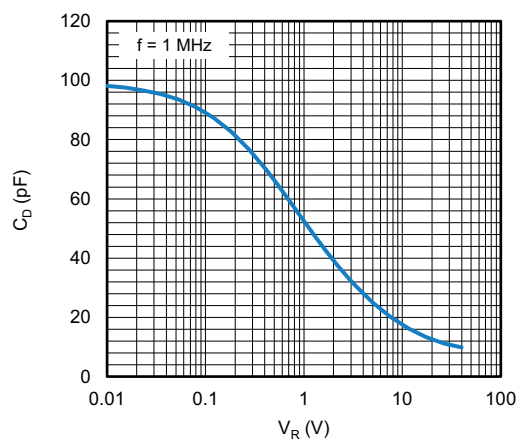


Fig. 2 - Typical Capacitance vs. Reverse Voltage

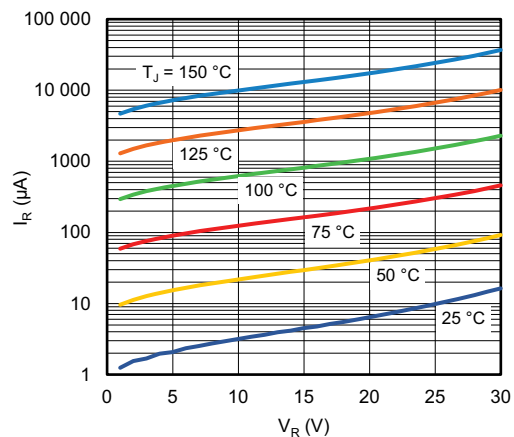
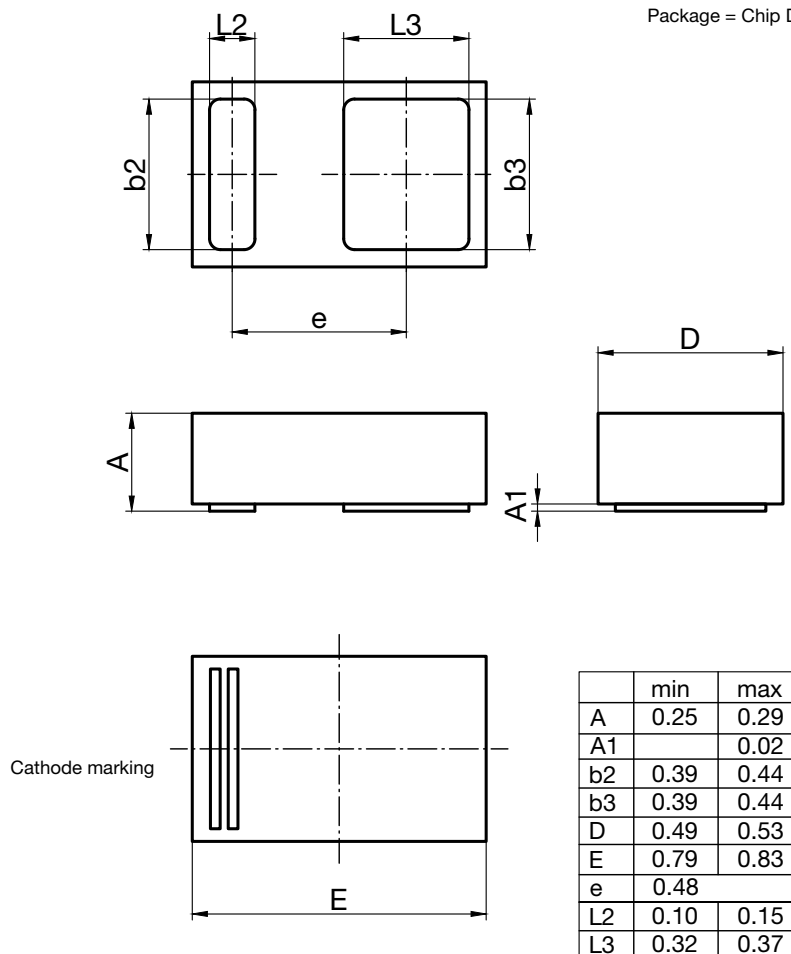


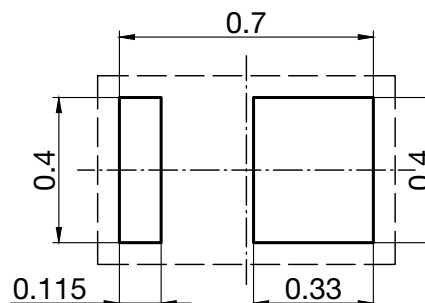
Fig. 3 - Typical Reverse Leakage Current vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters: **CLP0805-2L**

Package = Chip Dimensions in mm



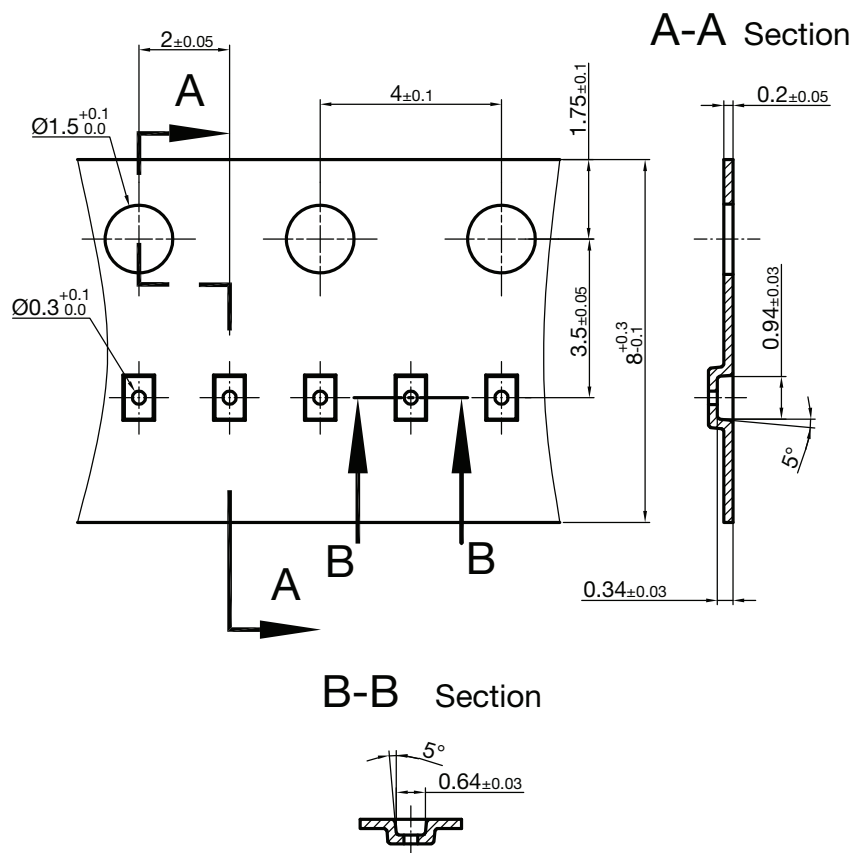
foot print recommendation:



Document no.:S8-V-3906.04-056 (4)
Created - Date: 03. July 2018
Rev.1 - Date: 19. Dec. 2023
23235

Footprint and soldering recommendation:

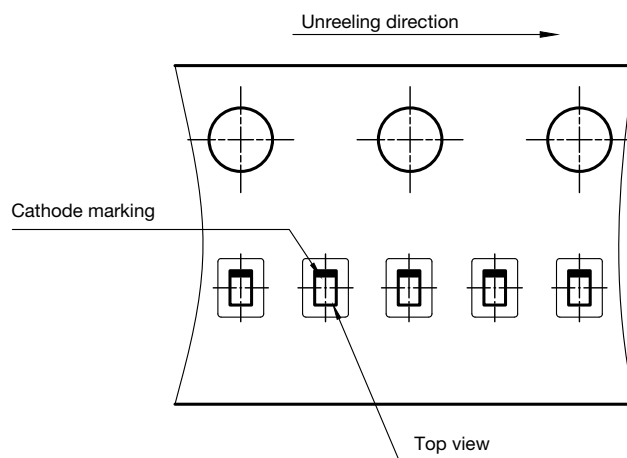
please see Application Note: www.vishay.com/doc?85917

CARRIER TAPE in millimeters: **CLP0805-2L**


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created Date: 03.07.2018
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Cummulative tolerances of 10 sprocket holes is ± 0.2 mm

ORIENTATION IN CARRIER CLP0805-2L



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created Date: 03.07.2018
Rev. 1: 19.12.2023
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