

N-Channel 75 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ)
75	0.0048 at $V_{GS} = 10$ V	90 ^d	105
	0.006 at $V_{GS} = 8$ V	90 ^d	

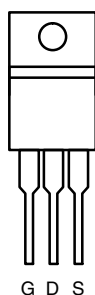
FEATURES

- TrenchFET® Power MOSFET
- 175 °C Junction Temperature
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC

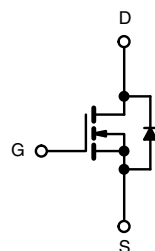

RoHS
COMPLIANT

APPLICATIONS

- Power Supply
 - Half-Bridge
 - Secondary Synchronous Rectification
- Industrial

TO-220AB


Top View

Ordering Information: SUP90N08-4m8P-E3 (Lead (Pb)-free)


N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	75	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175$ °C)	I_D	90 ^d	A
		90 ^d	
Pulsed Drain Current	I_{DM}	240	
Avalanche Current	I_{AS}	70	
Single Pulse Avalanche Energy ^a	E_{AS}	245	mJ
Maximum Power Dissipation ^a	P_D	300 ^b	W
		3.75	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) ^c	R_{thJA}	40	°C/W
Junction-to-Case (Drain)	R_{thJC}	0.5	

Notes:

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).
- Package limited.

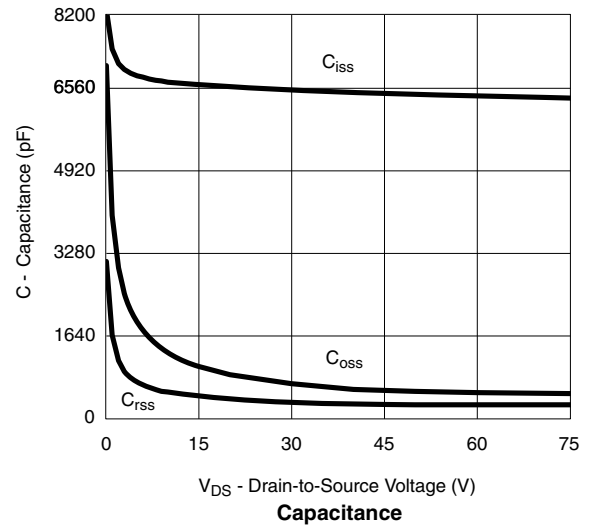
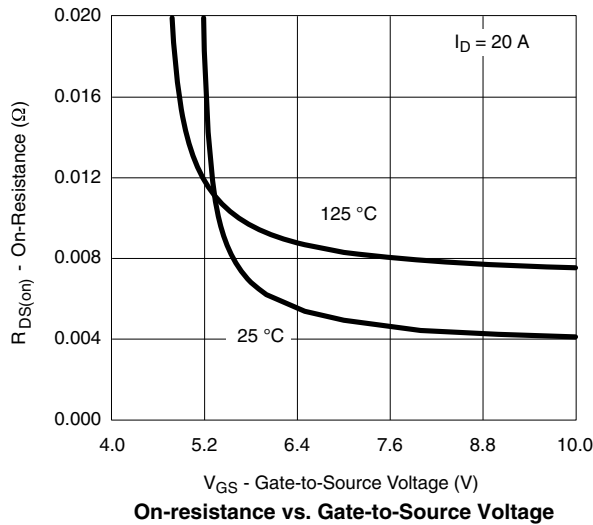
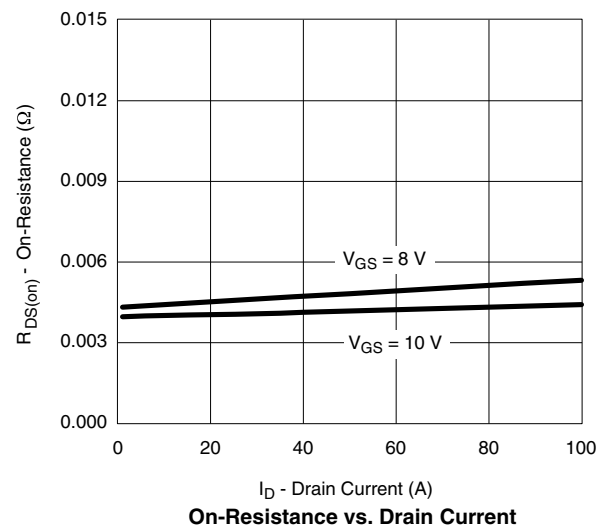
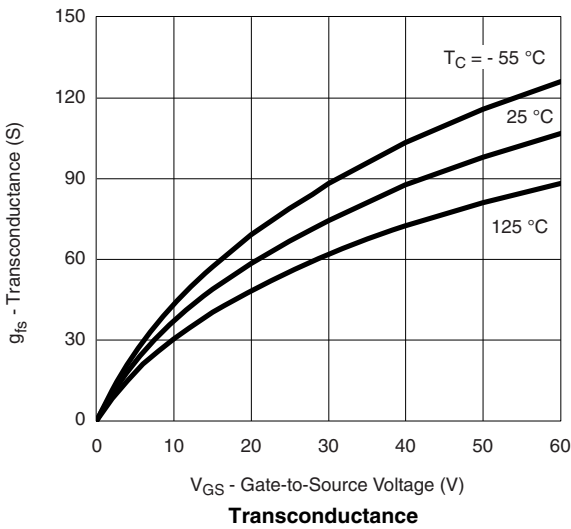
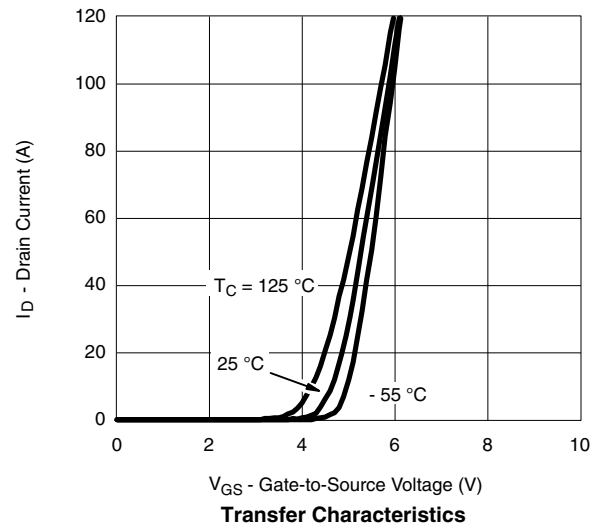
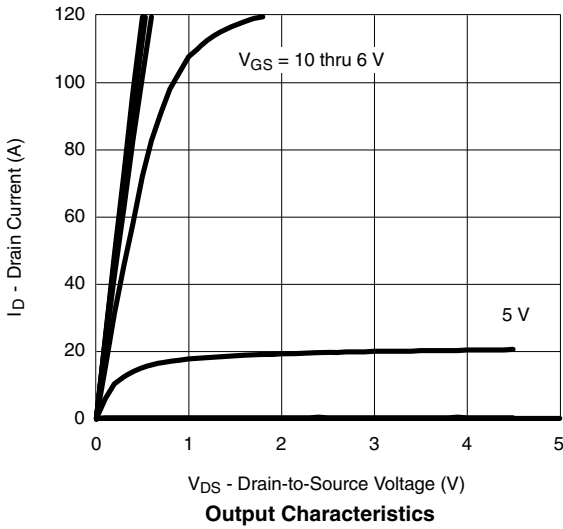
SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{DS} = 0 V, I _D = 250 μA	75			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2		4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 250	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 75 V, V _{GS} = 0 V			1	μA
		V _{DS} = 75 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 75 V, V _{GS} = 0 V, T _J = 150 °C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 10 V, V _{GS} = 10 V	70			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.004	0.0048	Ω
		V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C			0.0096	
		V _{GS} = 8 V, I _D = 20 A, T _J = 150 °C			0.0106	
		V _{GS} = 8 V, I _D = 20 A		0.0046	0.006	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 20 A		58		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 40 V, f = 1 MHz		6460		pF
Output Capacitance	C _{oss}			571		
Reverse Transfer Capacitance	C _{rss}			275		
Total Gate Charge ^c	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 85 A		105	160	nC
Gate-Source Charge ^c	Q _{gs}			32		
Gate-Drain Charge ^c	Q _{gd}			28		
Gate Resistance	R _g	f = 1 MHz		1.3	2.6	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 0.4 Ω I _D ≅ 85 A, V _{GEN} = 10 V, R _g = 1 Ω		23	35	ns
Rise Time ^c	t _r			17	26	
Turn-Off Delay Time ^c	t _{d(off)}			34	52	
Fall Time ^c	t _f			8	15	
Source-Drain Diode Ratings and Characteristics (T _C = 25 °C) ^b						
Continuous Current	I _S				85	A
Pulsed Current	I _{SM}				240	
Forward Voltage ^a	V _{SD}	I _F = 30 A, V _{GS} = 0 V		0.85	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 75 A, dI/dt = 100 A/μs		68	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}			2.6	4	A
Reverse Recovery Charge	Q _{rr}				88	132

Notes:

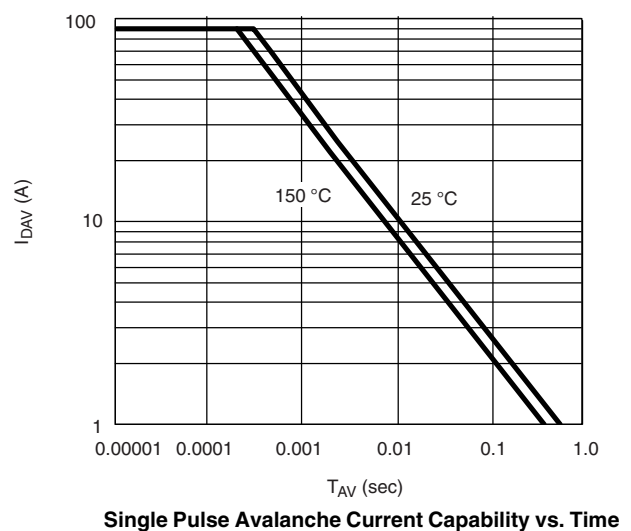
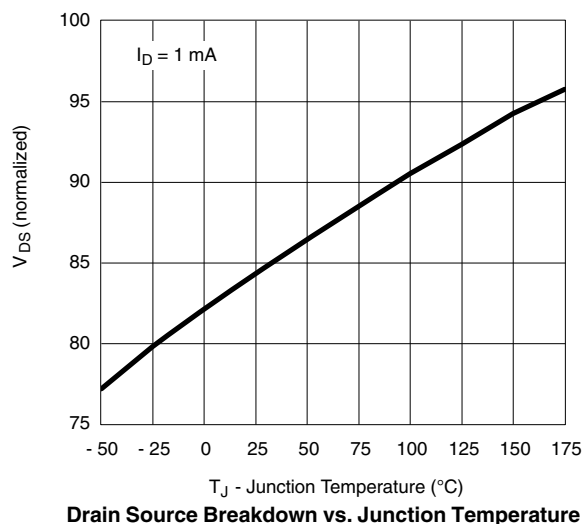
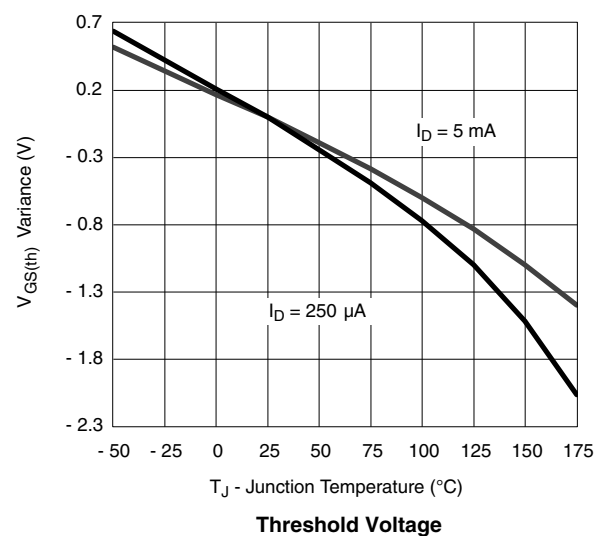
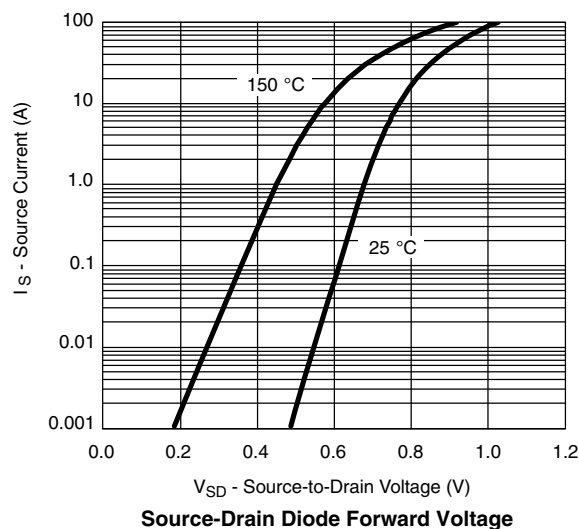
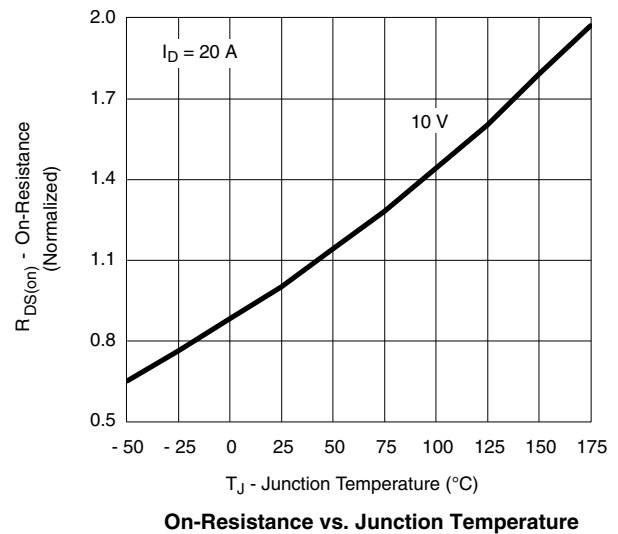
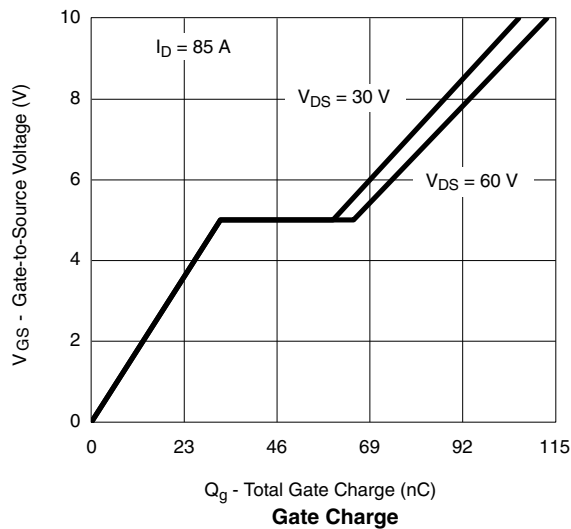
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

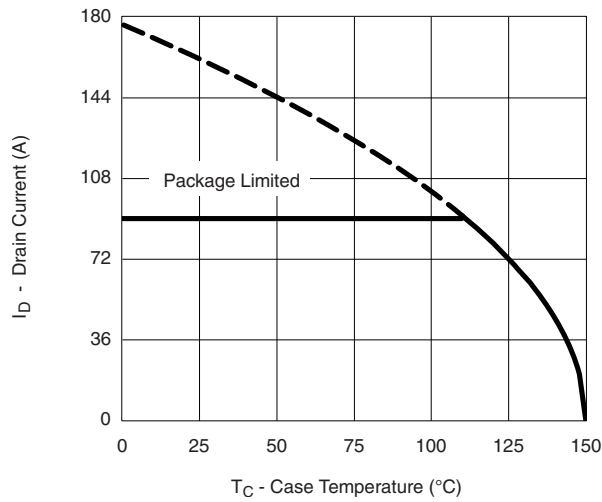
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



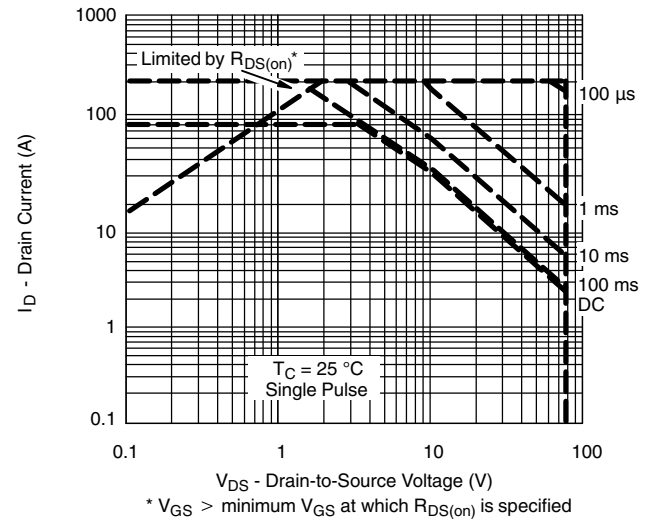
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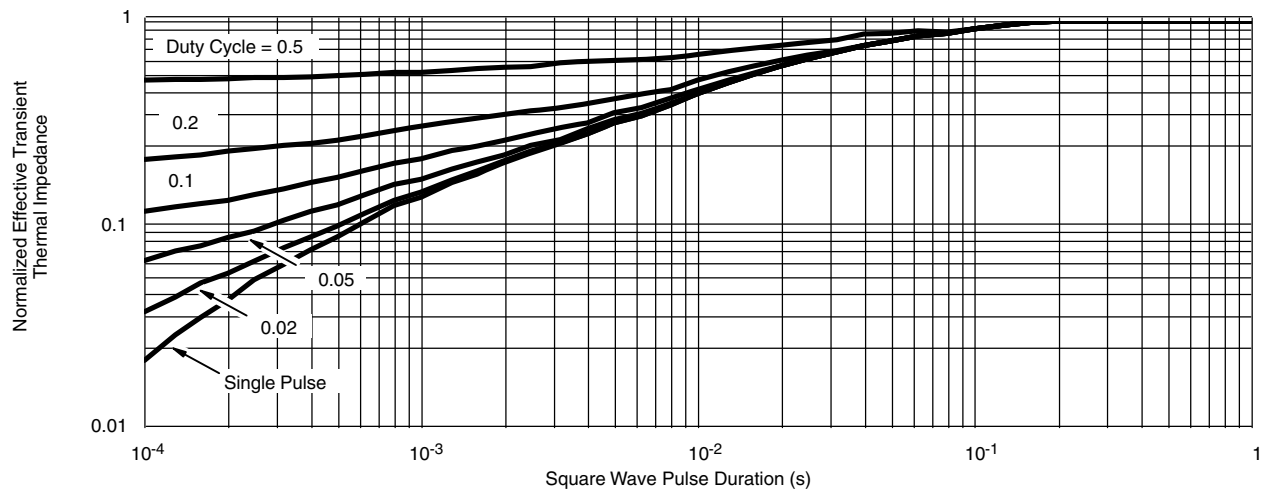
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Maximum Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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