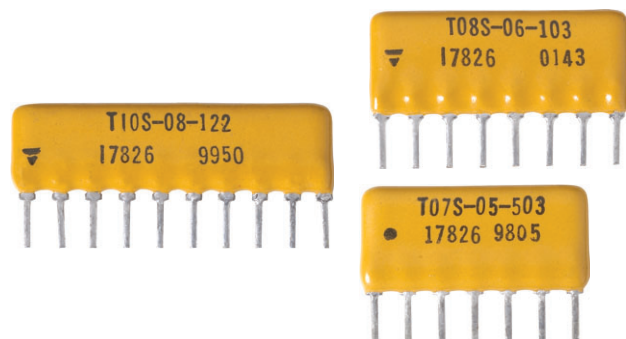


Thick Film Resistor Networks, Single-In-Line, Conformal Coated SIP



FEATURES

- 4 bit to 8 bit, R/2R ladder networks for D/A and A/D converter with bi-polar or CMOS switches
- Reduces total assembly costs
- Resistor element protected by tough epoxy conformal coating
- Thick film resistive elements
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS*
Available
**HALOGEN
FREE**

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details.

STANDARD ELECTRICAL SPECIFICATIONS

GLOBAL MODEL / PIN NO.	BITS	POWER RATING ELEMENT $P_{70^{\circ}\text{C}}$ W	RESISTANCE RANGE ⁽¹⁾ Ω	TOLERANCE $\pm \%$	TEMPERATURE COEFFICIENT (-55 °C to 125 °C) $\pm \text{ppm}/^{\circ}\text{C}$	LINEARITY (-55 °C to 125 °C) $\pm \text{LSB}$
T06S	04	0.050	50 to 1M	2	100	$\pm 0.5 \text{ LSB}$
T07S	05					
T08S	06					
T09S	07					
T10S	08					

Note

⁽¹⁾ 5K, 10K, 25K, 50K, and 100K are standard, other values available on special order

GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: T10S08100KRB (preferred part number format)

T	1	0	S	0	8	1	0	0	K	R	B
GLOBAL MODEL		NUMBER OF BITS		RESISTANCE VALUE (R)				TERMINAL FINISH		PACKAGING	
T06S = 6 pins		04 = 4 bits (6 pins)		R = Ω				R = Sn60/Pb40		B = bulk	
T07S = 7 pins		05 = 5 bits (7 pins)		K = k Ω				C = Sn95.5/Ag3.9/Cu0.6			
T08S = 8 pins		06 = 6 bits (8 pins)		M = M Ω							
T09S = 9 pins		07 = 7 bits (9 pins)		5K00 = 5 k Ω							
T10S = 10 pins		08 = 8 bits (10 pins)		5K10 = 5.1 k Ω							
				100K = 100 k Ω							
				Reference schematic							
				if R = 5 k Ω , then 2R = 10 k Ω							
				if R = 100 k Ω , then 2R = 200 k Ω							

Historical Part Numbering: T10S08104 (will continue to be accepted)

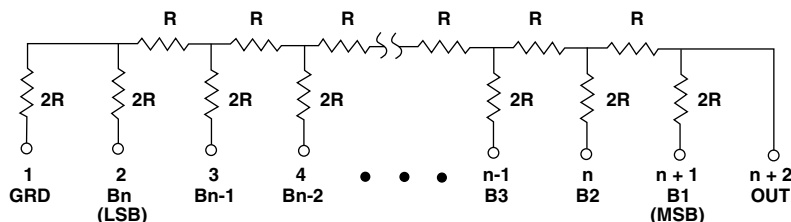
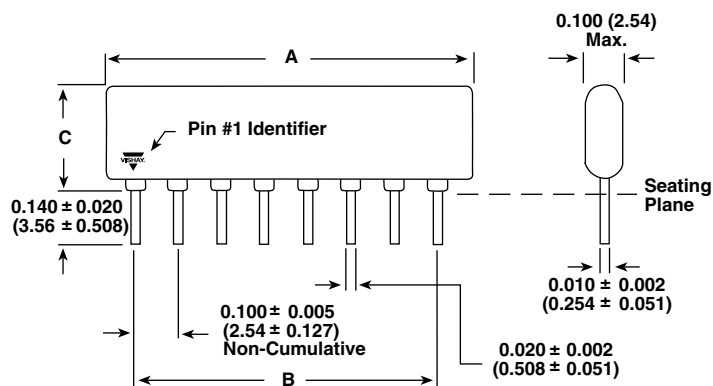
T10S	08	104	
HISTORICAL MODEL	NUMBER OF BITS	RESISTANCE VALUE (R)	TERMINAL FINISH

Note

⁽¹⁾ For additional information on packaging, refer to the "Through-Hole Network Packaging" document (www.vishay.com/doc?31542)

SCHEMATIC

n Bits:
n = 4 thru 8


DIMENSIONS in inches (millimeters)


NUMBER OF PINS	A (Max.)	B ± 0.005 (0.127)	C (Max.)
6	0.590 (14.99)	0.500 (12.70)	0.350 (8.89)
7	0.690 (17.53)	0.600 (15.24)	0.350 (8.89)
8	0.790 (20.07)	0.700 (17.78)	0.350 (8.89)
9	0.890 (22.61)	0.800 (20.32)	0.350 (8.89)
10	0.990 (25.15)	0.900 (22.86)	0.350 (8.89)



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