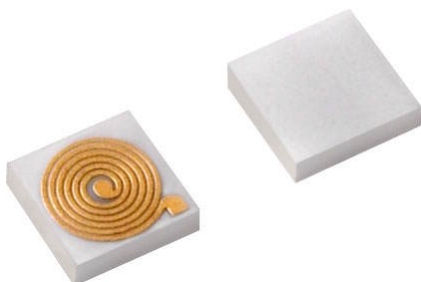


High Frequency RF Spiral Inductor for Wire Bonded Assemblies



PSC series RF spiral inductors are designed for RF circuits that require wire bondable components. High precision equivalent circuit modeling enables accurate computer simulation of component performance. Additional values and form factors available upon request.

FEATURES

- Wire bond assembly
- Small size: 45 mils x 45 mils x 10 mils
- Low DCR, high Q
- Low parasitic capacitance, high SRF



APPLICATIONS

- RF choking for DC biasing
- RF tuning circuits
- Lumped element filters

STANDARD ELECTRICAL SPECIFICATIONS

PARAMETER	VALUE	UNIT
Inductance Range	0.001 to 0.1	μH
Tolerance ⁽¹⁾	± 20	%
Max. Power Handling	250	mW
Operating Temperature	-55 to +125	°C
Storage Temperature	-55 to +125	°C

Note

⁽¹⁾ Main source of value tolerance is due to variation in wire bonds. See "text fixture" section below

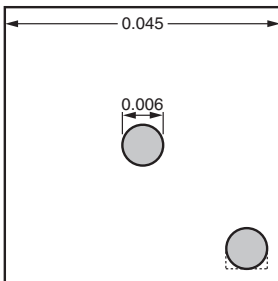
RF CHARACTERISTICS - TYPICAL VALUES

PART NUMBER	INDUCTANCE (nH)		DCR (Ω)	IN-CIRCUIT INDUCTANCE ⁽¹⁾ (nH)	IN-CIRCUIT DCR ⁽¹⁾ (Ω)	Q (UNITLESS)		SRF (GHz)
	250 MHz	1000 MHz				250 MHz	1000 MHz	
PSC3400C	3.4	3.4	0.26	6	0.36	8	14	10.2
PSC1050B	10.5	10.5	0.9	12	1	10	16	5.5
PSC1250B	12.5	12.5	0.9	15	1	12	18	5.0
PSC1800B	18	18	1.25	20.5	1.35	13	15	4.2
PSC4500B	45	50	3.4	47.5	3.5	13	8	2.4

Note

⁽¹⁾ Including the added inductance and resistance of typical bond wires. See equivalent circuit section

DIMENSIONS in inches (millimeters)

			
LENGTH	WIDTH	THICKNESS	BOND PAD DIAMETER
0.045 ± 0.002 (1.143 ± 0.051)	0.045 ± 0.002 (1.143 ± 0.051)	0.010 ± 0.001 (0.254 ± 0.0254)	0.006 (0.152)

MECHANICAL SPECIFICATIONS

Chip Substrate Material	Alumina 99.6 %
Conductor Material	Gold
Conductor Thickness	5 $\mu\text{m} \pm 10\%$
Bond Pad Diameter ⁽¹⁾	6 mil

Note

⁽¹⁾ Bond pad geometry can be modified upon request

GLOBAL PART NUMBER INFORMATION

Global Part Number: PSC3400CL4A1GNHW1

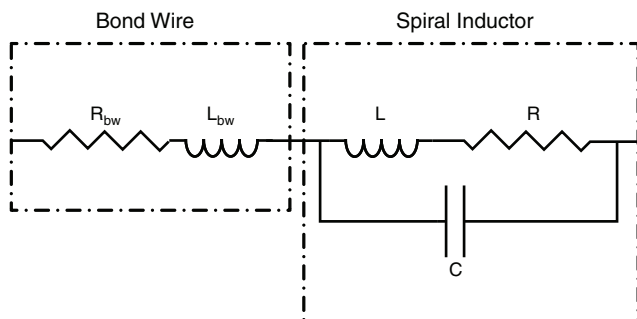
Global Part Number Description: PSC 3.4 nH 25 % 45 x 45 x 10 Au None H W1

P	S	C	4	0	0	0	C	L	4	A	1	G	N	H	W	1
MODEL	INDUCTANCE (nH)	INDUCTANCE MULTIPLIER CODE	TOLERANCE CODE	SIZE	SUBSTRATE	THICKNESS (mil)	TERMINATION	BACK METAL	TEST CLASS	PACKAGING CODE						
PSC	The first 4 digits are significant figures of inductance	C = 0.001 B = 0.01 A = 0.1	M = 20 % L = 25 %	4 = 45 x 45	A = alumina	1 = 10	G = au	G = au N = none	H = class H K = class K	W1 = waffle pack, 1 min., 1 mult. TS = tape and reel 100 min., 1 mult.						

EQUIVALENT CIRCUIT

The inductor's spiral trace presents a substantial amount of series resistance, and the close spacing of the spiral turns presents measurable amounts of stray capacitance that interact with the inductive characteristics.

A useful tool commonly used to model the behavior of electronic components at high frequency is the equivalent circuit model. While the equivalent circuit model accurately predicts the reactive part of the total impedance, it fails to determine the real part of the response at high frequency. This is due to the fact that the model does not include skin and proximity effects that significantly increase the real part of the impedance as the frequency rises.



L: Desired inductance
R: Parasitic self resistance
C: Parasitic shunt capacitance
R_{bw}: Bond wire self resistance

Fig. 1 - Equivalent Circuit

The equivalent circuit values are presented in the table below:

EQUIVALENT CIRCUIT MODEL 2				
INDUCTANCE VALUE L (nH)	DCR R (Ω)	SHUNT CAP. C (fF)	TYP. R _{bw} ⁽¹⁾ (Ω)	TYP. L _{bw} ⁽¹⁾ (nH)
3.4	0.26	71	0.1	2.5
10.5	0.9	80	0.1	2.5
12.5	0.9	81	0.1	2.5
18	1.25	84	0.1	2.5
45	3.4	92	0.1	2.5

Note

⁽¹⁾ Typical bond wires are approximated as being 1.25 mil in diameter gold, totaling a length of 3 mm. The resistance listed above includes the added effect of the bond wire adhesion to the substrate and component

TYPICAL COMPONENT PERFORMANCE

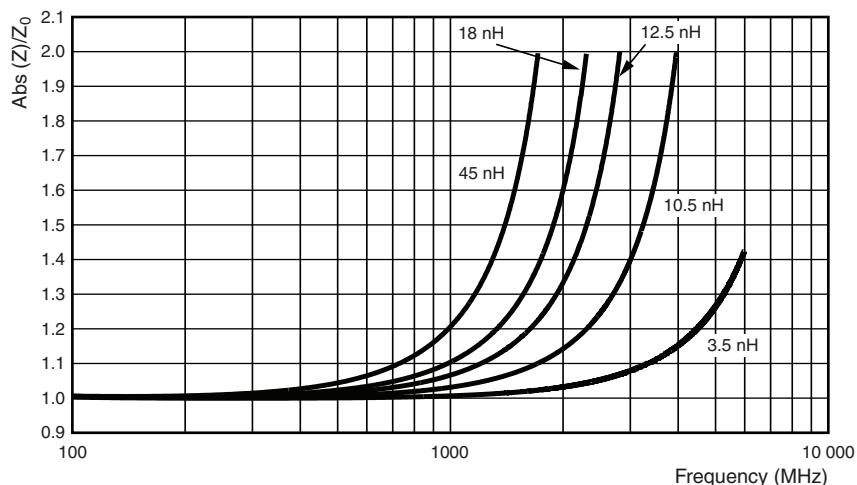


Fig. 2 - Relative Impedance vs. Frequency

LAYOUT CONSIDERATIONS

The RF spiral inductor is electrically connected to the circuit by wire bonds. All wire bonds add parasitic inductance and resistance, as shown in Figure 1.

It is important to note that setup variations might affect the performance of the component. Special care must be given to minimize these effects by careful design of the component host circuit board. The following aspects should be considered:

- Bond wire length should be minimized. The bond wire added inductance can be roughly estimated at 0.75 nH per mm of 1.25 mil diameter gold wire.
- Any ground plane directly under the component will increase the parasitic shunt capacitance. This will cause self-resonance at lower frequencies.
- The epoxy used to attach the component is the limiting factor in power handling. For applications that require high power handling it is recommended to use high temperature epoxy and to ensure adequate heat sinking.



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