



Sample Kit

Thick Film Resistors

DTO35 and D2TO35



FEATURES

- AEC-Q200 qualified
- 35 W at 25 °C case temperature for DTO35
- 35 W at 25 °C case temperature for D2TO35
- Surface mounted resistor - TO-252 (DPAK) style package for DTO35
- Surface mounted resistor - TO-263 (D²PAK) style package for D2TO35
- Wide resistance range: 0.01 Ω to 550 k Ω
- Non inductive
- Resistor isolated from metal tab
- Solder reflow secure at 270 °C / 10 s
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT

This sample kit includes a selection of resistor values covering a representative range. The resistors are arranged in ascending order of ohmic value and clearly marked for intuitive identification. Designed for engineers and designers, this kit allows quick validation of high-performance SMD power resistors engineered to operate reliably in the most challenging and demanding environments.

SAMPLE KIT SPECIFICATIONS	
Laboratory kit	DTO D2TO SAMPLE KIT
Resistor type DTO at temperature coefficient	± 150 ppm/ $^{\circ}$ C
Resistor type D2TO at temperature coefficient	± 150 ppm/ $^{\circ}$ C
Tolerance	± 1 %
Resistance value	1.0 Ω / 2.2 Ω / 10 Ω / 22 Ω / 47 Ω / 100 Ω / 220 Ω / 470 Ω / 1 k Ω / 4.7 k Ω / 22 k Ω / 47 k Ω
Number of resistance values	12
Quantity	4 resistors per value and model = 96 resistors in total
DTO PCB	1 PCB designed for 4 resistors test: parallel or series
D2TO PCB	1 PCB designed for 4 resistors test: parallel or series
Ordering part number	ACCLFFDTOD2TOSPLKT

Note

- For more detailed specifications please check the following documents:
 DTO35: www.vishay.com/doc?51078
 D2TO35: www.vishay.com/doc?51058
 Application note: www.vishay.com/doc?52027

TEST RECOMMENDATIONS

Power maximum DTO35: 4.5 W per piece on FR4 HTG PCB

Power maximum D2TO35: 3.5 W per piece on FR4 HTG PCB

The FR4 HTG PCB included in the kit covers two mountings: one is with a serial mounting, the other is a parallel mounting. Do not mount DTO or D2TO on both sides or the thermal performance will be deteriorated.

FOOTPRINT

We recommend the minimum footprints for the solderable contact area.

